REMARKS

Claim 1 has been amended to include the limitations which the Board of Appeals suggested were omitted from the claims on appeal. Now, the claim is explicit that the first processor-based system receives first information from the second processor-based system about second information available for transmission to the first processor-based system from the second processor-based system. This first information also enables the first processor-based system to determine if it has the information in a local cache. If the first processor-based system has the information in a local cache, then it declines the transmission of the second information. If the first processor-based system does not have the second information in the local cache, as determined using the first information, then it accepts the transmission of the second information from the second processor-based system.

Maddalozzo's information about whether it has a newer version of the file, discussed in the Board opinion, could not possibly meet this limitation since the first information must be about the second information and must also be sufficient to enable the first processor-based system to determine if it has the information on board in the local cache.

Therefore reconsideration of the claims as amended is respectfully requested.

Respectfully submitted,

Date: September 28, 2007

Timothy N. Trop, Reg. No. 28,994 TROP, PRUNER & HU, P.C.

8554 Katy Freeway, Ste. 100

Houston, TX 77024

713/468-8880 [Phone]

713/468-8883 [Fax]

Attorneys for Intel Corporation